Interference search for 10/042,281 gr

				500	10/0	142,20° g
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	103	(semiconductor and die and heat and (conduit or via or channel or passage) and (material or metal or silver or copper) and heat).clm.	USPAT	OR	OFF	2005/09/06 17:26
L2	189	(semiconductor and die and heat and (conduit or via or channel or passage) and (material or metal or silver or copper) and heat).clm.	US-PGPUB; USPAT	OR	OFF	2005/09/06 17:26
L3	64	(semiconductor and die and heat and (conduit or via or channel or passage) and (material or metal or silver or copper) and heat and (localized or proximate or adjacent)).clm.	US-PGPUB; USPAT	OR	OFF	2005/09/06 17:26
L4	52	(semiconductor and die and heat and (conduit or via or channel) and (material or metal or silver or copper) and heat and (localized or proximate or adjacent)).clm.	US-PGPUB; USPAT	OR	OFF	2005/09/06 17:27
L5	14	(semiconductor and die and heat and (conduit or via or channel) and (material or metal or silver or copper) and heat and (localized or proximate or adjacent) and (dissipate or conduct or transfer)). clm.	US-PGPUB; USPAT	OR	OFF	2005/09/06 17:27

plus search done.

10042281\_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10042281 on September 27, 2004

(0 OR, 5 XR)5 257/E23.004 257: ACTIVE SOLID-STATE DEVICES Class 257/E23.001 PACKAGING, INTERCONNECTS, AND MARKINGS FOR SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO) .Mountings, e.g., nondetachable insulating substrates (EPO) 257/E23.003 .. Characterized by shape (EPO) 257/E23.004 5 257/E23.069 (0 OR, 5 XR)257: ACTIVE SOLID-STATE DEVICES Class PACKAGING, INTERCONNECTS, AND MARKINGS FOR SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO) 257/E23.001 .Arrangements for conducting electric current to or from solid-state body in operation, e.g., 257/E23.01 leads, terminal arrangements (EPO) 257/E23.023 ... Consisting of soldered or bonded constructions (EPO) ...Leads, i.e., metallizations or lead frames 257/E23.06 on insulating substrates, e.g., chip carriers (EPO) ....Additional leads joined to metallizations 257/E23.068 on insulating substrate, e.g., pins, bumps, wires, flat leads (EPO)
....Spherical bumps on substrate for external 257/E23.069 connection, e.g., ball grid arrays (BGA) (EPO) (0 OR, 4 XR)257/E23.101 257 : ACTIVE SOLID-STATE DEVICES .. For integrated circuit devices, e.g., power 257/E23.079 bus, number of leads (EPO) .Arrangements for cooling, heating, ventilating 257/E23.08 or temperature compensation; temperature-sensing arrangements (EPO) ...Selection of materials, or shaping, to 257/E23.101 facilitate cooling or heating, e.g., heat sinks (EPO) (1 OR, 2 XR)257/714 257 : ACTIVE SOLID-STATE DEVICES Class .with large area flexible electrodes in press 257/688 contact with opposite sides of active semiconductor chip and surrounded by an insulating element, e.g., ring .With provision for cooling the housing or its 257/712 contents 257/714 ..Liquid coolant 257/737 (2 OR, 1 XR)257 : ACTIVE SOLID-STATE DEVICES class COMBINED WITH ELECTRICAL CONTACT OR LEAD 257/734 257/737 .Bump leads 257/E21.508 (0 OR, 3 XR)257 : ACTIVE SOLID-STATE DEVICES Class PROCESSES OR APPARATUS ADAPTED FOR MANUFACTURE 257/E21.001 OR TREATMENT OF SEMICONDUCTOR OR SOLID-STATE DEVICES OR OF

PARTS THEREOF (EPO)

Page 1

257/E21.002

.Manufacture or treatment of semiconductor device (EPO)

```
10042281_CLSTITLES
                            ..Device having at least one potential-jump
           257/E21.04
                                    barrier or surface barrier, e.g., PN junction,
depletion
                                    layer, carrier concentration layer (EPO)
                            ... Assembling semiconductor devices, e.g.,
           257/E21.499
                                  packaging, including mounting, encapsulating, or
treatment
                                  of packaged semiconductor (EPO)
                            ....Attaching or detaching leads or other
           257/E21.506
                                  conductive members, to be used for carrying current to
or
                                 from device in operation (EPO)
                            .....Formation of contacts to semiconductor by
           257/E21.507
                                use of metal layers separated by insulating layers,
e.g.,
                                self-aligned contacts to source/drain or emitter/base
(EPO)
                            .....Forming solder bumps (EPO)
           257/E21.508
     257/E23.068
                      (0 \text{ OR}, 3 \text{ XR})
                     237 :
                            ACTIVE SOLID-STATE DEVICES
           257/E23.001
                            PACKAGING, INTERCONNECTS, AND MARKINGS FOR
                                    SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO)
                            Arrangements for conducting electric current to or from solid-state body in operation, e.g., leads, terminal arrangements (EPO)

..Consisting of soldered or bonded
           257/E23.01
           257/E23.023
                                  constructions (EPO)
                            ...Leads, i.e., metallizations or lead frames
           257/E23.06
                            on insulating substrates, e.g., chip carriers (EPO)
....Additional leads joined to metallizations
           257/E23.068
                               on insulating substrate, e.g., pins, bumps, wires, flat
                               leads (EPO)
                    (0 OR, 2 XR)
052 : STATIC STRUCTURES
       52/287.1
           Class
           52/287.1
                           CONDUIT, TRIM, OR SHIELD MEMBER AT CORNER
                    (1 OR, 1 XR)
052 : STATIC STRUCTURES
  2
           class
                            EXTERIOR-TYPE FLASHING
           52/58
                     (0 OR, 2 XR)
052 : STATIC STRUCTURES
  2
       52/631
           class
                            CORNER FORMED BY LAMINATE WITH BENT FACING
           52/631
                               SECTION
                      (2 OR, 0 XR)
  2
       99/348
                     099 :
                             FOODS AND BEVERAGES: APPARATUS
           Class
           99/324
                            COOKING
           99/348
                            .With stirring
                      (0 \text{ OR}, 2 \text{ XR})
     101/129
                             PRINTING
           class
                     101 :
           101/114
                            STENCILING
           101/129
                            .Processes
                    (0 OR, 2 XR)
126 : STOVES AND FURNACES
     126/672
           Class
           126/569
126/634
                            SOLAR HEAT COLLECTOR
                            .with means to convey fluent medium through
                                  collector
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Page 2

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10042281_CLSTITLES
                           ..Conduit absorber structure
           126/651
           126/672
                           ...Circular nonmetallic conduit
                     (0 OR, 2 XR)
.64: METAL FOUNDING
     164/122
           Class
                    164:
           164/1
                           PROCESS
                           .Shaping liquid metal against a forming surface
           164/47
                           ... Controlling solidification (other than
           164/122
                              ambient cooling)
                     (0 \text{ OR}, 2 \text{ XR})
  2 164/133
                    164: METAL FOUNDING
           class
           164/1
                           PROCESS
           164/47
                           .Shaping liquid metal against a forming surface
                           .. Introduction control or manipulation of
                              charge
     164/900
                     (0 OR, 2 XR)
                    164: METAL FOUNDING
           Class
           164/900
                          RHEO-CASTING
  2
     165/46
                     (0 \text{ OR}, 2 \text{ XR})
           Class
                    165 : HEAT EXCHANGE
                          FLEXIBLE ENVELOPE OR COVER TYPE
           165/46
                     (0 \text{ OR}, 2 \text{ XR})
     174/260
           class
                    174 : ELECTRICITY: CONDUCTORS AND INSULATORS
           174/68.1
                          CONDUITS, CABLES OR CONDUCTORS
                           .Preformed panel circuit arrangement (e.g.,
           174/250
                               printed circuit)
                           .. with electrical device
           174/260
                     (0 OR, 2 XR)
     257/692
                    257 : ACTIVE SOLID-STATE DEVICES
           Class
                           .with large area flexible electrodes in press
           257/688
                                contact with opposite sides of active semiconductor
chip
                                and surrounded by an insulating element, e.g., ring
           257/690
                           .with contact or lead
           257/692
                           .. with particular lead geometry
                     (0 OR, 2 XR)
     257/712
                    257 : ACTIVE SOLID-STATE DEVICES
           class
                           .with large area flexible electrodes in press
           257/688
                               contact with opposite sides of active semiconductor chip
                               and surrounded by an insulating element, e.g., ring
           257/712
                           .With provision for cooling the housing or its
                              contents
                     (0 \text{ or}, 2 \text{ xr})
    257/738
                    257 : ACTIVE SOLID-STATE DEVICES
           Class
           257/734
257/737
                           COMBINED WITH ELECTRICAL CONTACT OR LEAD
                           .Bump leads
           257/738
                           ..Ball shaped
    257/774
                     (0 \text{ OR}, 2 \text{ XR})
                   257 : ACTIVE SOLID-STATE DEVICES

COMBINED WITH ELECTRICAL CONTACT OR LEAD

Of specified configuration
           Class
           257/734
257/773
                           .. Via (interconnection hole) shape
           257/774
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## 10042281\_CLSTITLES

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257/778
                        (0 \text{ OR}, 2 \text{ XR})
                      257:
                              ACTIVE SOLID-STATE DEVICES
            class
            257/734
                              COMBINED WITH ELECTRICAL CONTACT OR LEAD
            257/778
                              .Flip chip
     257/780
                        (0 \text{ OR}, 2 \text{ XR})
            Class
                              ACTIVE SOLID-STATE DEVICES
                      257 :
            257/734
                              COMBINED WITH ELECTRICAL CONTACT OR LEAD .Ball or nail head type contact, lead, or bond
            257/780
                      \cdot (0 OR, 2 XR)
  2 257/E23.008
                       257 :
                              ACTIVE SOLID-STATE DEVICES
                              PACKAGING, INTERCONNECTS, AND MARKINGS FOR SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO)
            257/E23.001
                              .Mountings, e.g., nondetachable insulating substrates (EPO)
..Characterized by material or its electrical
            257/E23.003
            257/E23.005
                                   properties (EPO)
                              ... Semiconductor insulating substrates (EPO)
            257/E23.008
     257/E23.021
                        (0 \text{ OR}, 2 \text{ XR})
                       257 :
                              ACTIVE SOLID-STATE DEVICES
            Class
                              PACKAGING, INTERCONNECTS, AND MARKINGS FOR
            257/E23.001
                                       SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO)
                              Arrangements for conducting electric current to or from solid-state body in operation, e.g., leads,
            257/E23.01
                                      terminal arrangements (EPO)
                              ...Consisting of lead-in layers inseparably applied to semiconductor body (EPO)
            257/E23.012
                               ...Consisting of layered constructions
            257/E23.019
                                   comprising conductive layers and insulating layers,
e.g.,
                                   planar contacts (EPO)
                              ....Bump or ball contacts (EPO)
            257/E23.021
                        (0 \text{ OR}, 2 \text{ XR})
  2 257/E23.075
                       257 : ACTIVE SOLID-STATE DEVICES
            Class
                              PACKAGING, INTERCONNECTS, AND MARKINGS FOR SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES (EPO)
            257/E23.001
            257/E23.01
                               .Arrangements for conducting electric current
                                       to or from solid-state body in operation, e.g.,
leads,
                              terminal arrangements (EPO) ...Consisting of soldered or bonded
            257/E23.023
                               constructions (EPO)
...Leads, i.e., metallizations or lead frames
            257/E23.06
                              on insulating substrates, e.g., Chip carriers (EPO)
....Characterized by materials (EPO)
            257/E23.072
            257/E23.075
                               .....Conductive materials containing organic
                                  materials or pastes, e.g., for thick films (EPO)
     257/E23.088
                        (0 \text{ OR}, 2 \text{ XR})
                       257 : ACTIVE SOLID-STATE DEVICES
            Class
            257/E23.079
                              .. For integrated circuit devices, e.g.,
                                      bus, number of leads (EPO)
                               .Arrangements for cooling, heating, ventilating
            257/E23.08
                                    or temperature compensation; temperature-sensing
                              arrangements (EPO)
..Fillings or auxiliary members in containers or encapsulations selected or arranged to facilitate
            257/E23.087
                                   heating or cooling (EPO)
                               ...Cooling by change of state, e.g., use of
            257/E23.088
                                                 Page 4
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## 10042281\_CLSTITLES heat pipes (EPO)

2		OR, 2 XR) : ACTIVE SOLID-STATE DEVICESFor integrated circuit devices, e.g., power bus, number of leads (EPO)
	257/E23.08	Arrangements for cooling, heating, ventilating or temperature compensation; temperature-sensing
	257/E23.087	or encapsulations selected or arranged to facilitate heating or cooling (EPO)
	257/E23.09	Auxiliary̆ members in̆ containers characterized by their shape, e.g., pistons (EPO)
	257/E23.092	Auxiliary members in encapsulations (EPO)
2		OR, 2 XR) : ACTIVE SOLID-STATE DEVICES ASSEMBLIES CONSISTING OF PLURALITY OF INDIVIDUAL SEMICONDUCTOR OR OTHER SOLID-STATE DEVICES
(EPO)	)	
	257/E25.002	.All devices being of same type, e.g., assemblies of rectifier diodes (EPO)
	257/E25.022 257/E25.023	<pre>Devices having separate containers (EPO)Device consisting of plurality of   semiconductor or other solid-state devices or components</pre>
circu	uit	formed in or on common substrate, e.g., integrated device (EPO)
2	359/2 (0	OR, 2 XR)
	Class 359 359/1 359/2	: OPTICS: SYSTEMS HOLOGRAPHIC SYSTEM OR ELEMENT .Authentication
2	361/699 (2 Class 361	OR, 0 XR) : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES
	361/600	HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE ELECTRICAL COMPONENTS
	361/679 361/688 361/689 361/699	.For electronic systems and devicesWith cooling meansFluidLiquid
2	361/761 (0 Class 361	OR, 2 XR) : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES
	361/600	HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE ELECTRICAL COMPONENTS
	361/679	.For electronic systems and devices
	361/748 361/760 361/761	Printed circuit boardConnection of components to boardComponent within printed circuit board
2	361/764 (0 Class 361	OR, 2 XR) : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES
	361/600	HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE
	361/679	ELECTRICAL COMPONENTS .For electronic systems and devices Page 5

	361/748 361/760 361/761 361/764		10042281_CLSTITLESPrinted circuit boardConnection of components to boardComponent within printed circuit boardIntegrated circuit
2	361/820 Class	(1 361	OR, 1 XR): ELECTRICAL SYSTEMS AND DEVICES
	361/600		HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE ELECTRICAL COMPONENTS
	361/679 361/820		.For electronic systems and devicesFor semiconductor device
2	430/1	(2	OR, 0 XR)
	Class	430	: RADIATION IMAGERY CHEMISTRY: PROCESS, COMPOSITION, OR PRODUCT THEREOF
	430/1		HOLOGRAPHIC PROCESS, COMPOSITION, OR PRODUCT